

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application:

Application No.:

Filed:

Title:

Commissioner for patents
Washington, D.C. 20231

POWER OF ATTORNEY BY ASSIGNEE OF ENTIRE INTEREST
(REVOCATION OF PRIOR POWERS)

As assignee of record of each of the patent(s)/patent application(s) listed in the table of attachment A,

REVOCATION OF PRIOR POWERS OF ATTORNEY

all powers of attorney previously given in each of the listed patent applications are hereby revoked,
and

NEW POWER OF ATTORNEY

the following attorneys/agents are hereby appointed to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith: I hereby appoint all attorneys of Thomas, Kayden, Horstemeyer & Risley, LLP, who are listed under the USPTO Customer Number shown below as the attorneys to prosecute this application and to transact all business in the United States Patent and Trademark Office connected therewith, recognizing that the specific attorneys listed under that Customer Number may be changed from time to time at the sole discretion of Thomas, Kayden, Horstemeyer & Risley, LLP, and request that all correspondence about the application be addressed to the address filed under the same USPTO Customer Number.

24504

Patent Trademark Office

Please direct all future correspondence and telephone calls to:

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THOMAS, KAYDEN, HORSTEMEYER & RISLEY, L.L.P.
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ASSIGNEE OF ENTIRE INTEREST


MediaTek Inc.
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Hsin-Chu 300, Taiwan, R.O.C.

ASSIGNEE CERTIFICATION

The certification under 37 C.F.R. §3.73(b) establishing the right of assignee to take action is attached hereto along with documentation evidencing same. Further, in my official position with MediaTek Inc., I am authorized to sign documents and otherwise act on its behalf in connection with the management and handling of patent applications and other intellectual property matters.

Date: _____

8/22/07



Wei-Fu Hsu
Director of Legal & Intellectual Property Div.

Attachment A

No.	MediaTek's No.	Serial No.	Application Title	Filing Date	Assignment (Reel/Frame) Inventors to MediaTek	TKHR Ref. No.
1.	MTKI-04-072A	10/989,748	METHOD AND SYSTEM FOR EFFICIENT COMMAND PERFORMANCE OF MULTIPLE UNITS SHARING THE SAME DATA INTERFACE PORT	11-16-2004	016003/0716	
2.	MTKI-98-006A	09/330,230	METHOD AND SYSTEM FOR PROGRAMMING A PERIPHERAL FLASH MEMORY VIA AN IDE BUS	06-10-1999	010044/0248	
3.	MTKI-98-006A-1	10/806,086	METHOD AND SYSTEM FOR PROGRAMMING A PERIPHERAL FLASH MEMORY VIA AN IDE BUS	03-20-2004	010044/0248 (as in the parent application No. 09/330,230)	
4.	MTKI-98-006A-2	11/251,013	METHOD AND SYSTEM FOR PROGRAMMING A PERIPHERAL FLASH MEMORY VIA AN IDE BUS	10-13-2005	010044/0248 (as in the parent application No. 09/330,230)	